LED Packaging
Report 2011

Packaging typically accounts for 20% to 60% of the packaged LED total cost and therefore represents the single largest opportunity for cost reduction.

MORE THAN $2B WILL BE INVESTED OVER THE 2011-2016 PERIOD IN NEW EQUIPMENT FOR LED PACKAGING (LLO, PERMANENT BONDING, SINGULATION AND TESTING)

LED packagers are still using mostly retrofitted equipment from the IC industry and relying on existing technology solutions and materials to improve LED cost of ownership and performance. While this has allowed LED manufacturers to benefit from decades of research and development and investments in the IC industry, it also constrains the industry into the space defined by existing technology platforms that are not optimized for the specific needs of LEDs. But the industry has now gained enough momentum and reached a critical mass to entice equipment and material providers into developing dedicated solutions for LED manufacturing. Many dedicated solutions are emerging from existing and new players that will allow significant reduction in LED manufacturing cost through improved yields, throughputs and material efficiency.

LED Packaging Equipment Market Revenues

LED packaging equipment market forecast to 2016. Including substrate separation (LLO), permanent bonders, die separation (diamond scriber, laser scriber, blade dicer, laser dicer), in-line tester (Wafer, die and package inspection, probers, sorters, tapers...), die-bonder...etc...

LED PACKAGING EQUIPMENT OVER-INVESTMENT IN THE LAST 2 YEARS LEADING TO A SHORT DOWN-CYCLE STARTING IN 2012

No LED manufacturers want to get caught short of capacity when the general lighting market scheduled to reach $20B by 2020 takes off. Thus, an unprecedented investment cycle started toward the end of 2009 and will extend through early 2012. This cycle initiated in Korea is now essentially fueled by subsidies and other incentives in China, as the country is aggressively trying to position itself as a future leader in solid state lighting. New entrants are investing massive amounts of money in order to displace existing manufacturers. This will lead to a world averaged overcapacity that will briefly exceed 50% for some tools (ie: capacity utilization rate of <50%) by mid-2012. This in turn will cause a 12-18 month down cycle corresponding to the absorption of this overcapacity as well as some consolidations that will bring the industry back to more usual utilization rates of 80%. This down cycle will extend through mid-2013. Then we expect a new investment cycle to kick in to respond to further increasing demand for general lighting. This might lead to another shorter excess investment to be absorbed in 2016.
package substrate makers will see the fastest growth with a CAGR of 45% through the period. Phosphors will experience strong price pressure but still enjoy double digit growth with a CAGR of 12%. With the intensifying competition, players try to differentiate themselves by proposing an increasingly wide variety of technology options for LED packaging. Substrate material options as well as assembly and interconnection techniques abound as as many workarounds to the limiting patents of the established players. New players from the general semiconductor markets propose new solutions based on their respective capabilities. Similarly to IC packaging, new technologies for LED packaging add up to the existing ones without completely phasing them out. And there’s still a lot of room for innovation that could allow capturing more added value. For such products however, it remains paramount that the solution offers an overall reduction in cost of ownership ($/lumen) to LED manufacturers.

This report reviews the major challenges associated with the key LED packaging process steps, and insights are shared on the pros and cons of the various technology options and their future forecasted trends.

It focuses especially on the most recent technologies and market trends for high power LED and packages and arrays and provides quantification for various materials and equipment associated with each of those key steps. Trends are analyzed in detail including emerging technologies like Silicon substrates, Wafer level packaging, COB etc...
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The present document is 24 months after its publishing date: 27th June 2011

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